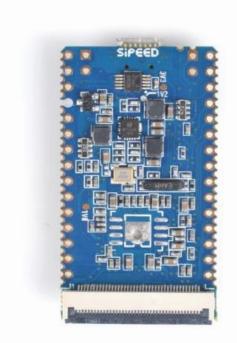


Sipeed Lichee Zero Datasheet_EN v1.0





Main features:

- CPU : Allwinner V3S ; ARM Cortex-A7 architecture ; Basic frequence 1.2Ghz
- RAM : SIP 64MB DDR2
- Memory: On-board TFcard slot / Flash SOP8 pads (Dual system boot way)
- Display: General 40P RGB LCD FPC 0.5mm Connector; Support resolution of 272x480, 480x800, 1024x600, etc.
- Interfaces: SDIO, UART, SPI, I²C, OTG USB, PWM, CSI, MIPI, etc.

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UPDATE		
	V1.0	Edited on April 23, 2020 ; Original document

	CDECIFICATION
	SPECIFICATION
CPU	Allwinner V3S; ARM Cortex-A7 architecture; Basic
_FU	frequence 1.2Ghz
RAM and Memory	SIP 64MB DDR2
	On-board SOP8 pads (Support system boot from Flash)
	On-board TFcard slot(Support system boot from TFcard)
	General 40P RGB LCD FPC 0.5mm Connector
	Common 40P 4.3/5/7-inch screencan be used directly(On-
Display	board LCD backlight driver circuit)
	Support resolution of 272x480 , 480x800 , 1024x600 , etc
	On board resistive touch screen chip
	Support video decoder for H.264 and JPEG/MJPEG
Video ou consideration	Support H.264 BP/MP/HP up to 1080p@30fps
Video processing capacity	Support H.264 output formats: NV21,NV12,YU12,YV12
	Support JPEG/MJPEG up to 1080p@30fps
	SDIO x2 (SDIO WiFi + Bt module is on sale)
	SPI x1
	TWI x2
Communication interfaces	UART x3
	100M Ether x1(含 EPHY)
	OTG USB x1
	MIPI CSI x1
	PWM x2
Other interfaces	LRADC x1
	Speakerx2 + Mic x1

SOFTWARE FEATURES	
Linux	Support Linux 3.4 BSP kernel Support Linux 4.16 Main-line kernel
Linux applications	Support QT, Python and other common Linux applications



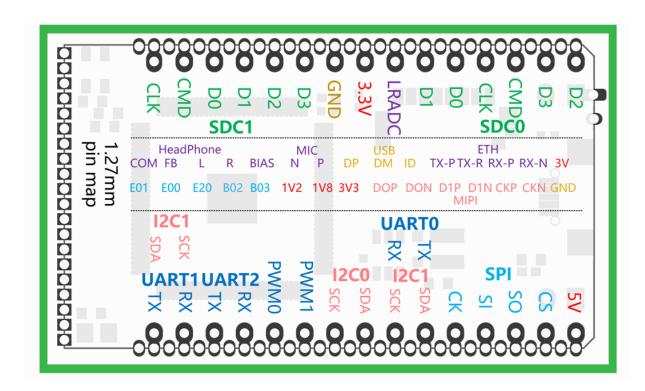
HARDWARE FEATURES	
	Micro USB: 5.0V ±0.2V
External supply voltage requirement	2.54mm dip pads: 3.7-5.0V ±0.1V
	1.25mm smt pads: 3.7-5.0V ±0.1V
External supply current requirement	> 300mA @ 5V
Measured current under various	1GHz linux no load running current: 100mA±10mA
working conditions	1GHz linux full load running current: 180mA±10mA;
Temperature rise	<30K
Range of working temperature	-30℃ ~ 85℃

V3s block diagram

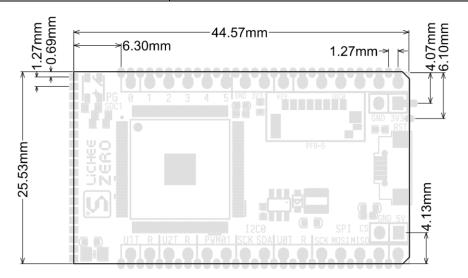
Connectivity	Memory	Audio Codec
SPI/TWI/UART	Integrated DRAM	
PWM	SPI NAND/NOR Flash	Display
LRADC	SD/SDIO	Display Engine 2.0
		RGB/CPU LCD
System	CPU	
Timer/HS-timer	OF O	ISP
8-ch DMA	38	Image
24MHz Clock		Image
	Video Engine	lmage Parallel CSI



Lichee Zero pin map



Size and weight information	
Length	44.6mm
Width	25.5mm
Height	6.0 mm
Weight	4.8±0.1g





Matters needing attention	
ESD measures	When using in a dry environment, please release static
	electricity on your body before touching the board
Screen	Before plugging in the screen, please make sure that the pin
	definition sequence of the 40p seat corresponds to the pin
	definition sequence of the screen to avoid burning the
	backlight of the screen
Start-up	Nano can boot from Tfcard or Flash.
	If you only connect the USB cable, nano will not start
System debugging serial port	UARTO (Refer to Lichee Nano pin map for specific location)

Target application scenario
IOT applications using complex communication interfaces and protocols
The application of human-computer interface which needs more beautiful and complex logic
Application scenarios requiring more computation (relative to common MCU)
Scenarios requiring rapid development using open source software under Linux
Geek players who want to balance volume, performance and ease of use
Entry level players and software engineers who want to do hardware DIY in a familiar language



RESOURCES	
Official Website	www.sipeed.com
Github	https://github.com/sipeed
BBS	https://bbs.sipeed.com/
Wiki	https://wiki.sipeed.com/
SDK and HDK	China users https://cn.dl.sipeed.com/LICHEE/Nano Global users https://dl.sipeed.com/LICHEE/Nano
E-mail(Technical Support)	support@sipeed.com
telgram link	https://t.me/sipeed
QQ Group	878189804



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